

AMENDMENTS IN THE CLAIMS**RECEIVED
CENTRAL FAX CENTER****OCT 31 2011**

1 1. (Canceled)

1 2. (Currently amended) The system according to claim 12, wherein the
2 switching of the individual packets of a call to any one respective transcoder of available
3 transcoders is a function of at least one predetermined parameter, and wherein the at
4 least one predetermined parameter comprises at least one of a state of each respective
5 transcoder, and a current load on the plurality of transcoders.

1 3. (Currently amended) The system according to claim 12, wherein the
2 switching of the individual packets of a call to any one respective transcoder of available
3 transcoders is on an as needed basis.

1 4. (Previously presented) The system according to claim 12, wherein a
2 plurality of digital signal processor (DSP) channels is formed by a set of internal AAL2
3 PVCs between the intermediate node and a set of transcoders, and wherein an
4 allocation of a respective DSP channel, of the plurality of DSP channels for a call is a
5 function of at least one predetermined parameter.

1 5. (Previously presented) The system according to claim 4, wherein the at
2 least one predetermined parameter comprises at least one of a state of the set of
3 transcoders, a current load on the set of transcoders, and a state of the set of internal
4 AAL2 PVCs.

1 6. – 11. (Canceled)

1 12. (Currently amended) A system for using asynchronous transfer mode
2 adaptation layer 2 (AAL2) switching within a wireless access gateway, comprising:
3 a plurality of external AAL2 permanent virtual circuits (PVCs);
4 a plurality of internal AAL2 PVCs;
5 a plurality of transcoders;
6 at least one intermediate node that terminates ~~operatively connected to~~ the
7 plurality of external AAL2 PVCs and the at least one intermediate node is operatively
8 connected to the internal AAL2 PVCs; and
9 ~~an algorithm that takes into account at least a current state of each of the~~
10 ~~plurality of transcoders and a current load of all of the plurality of transcoders;~~
11 a single packet switch control operatively connected to the at least one
12 intermediate node, the plurality of internal AAL2 PVCs and the plurality of transcoders;
13 wherein the single packet switch control is structured to ~~utilize the algorithm to~~
14 instruct the at least one intermediate node to switch individual AAL2 common part
15 sublayer (CPS)-Packets from the external AAL2 PVCs to the internal AAL2 PVCs, the
16 single packet switch control is structured to allocate individual channel identifiers (CIDs)
17 to transcoder channels on an as needed basis, and the single packet switch control is
18 structured to effect switching of individual packets from the external AAL2 PVCs and to
19 the internal AAL2 PVCs that allows for an even distribution of load among the
20 transcoders even if a load on the external AAL2 PVCs is uneven.

1 13. – 15. (Canceled)

- 1 16. (New) The system according to claim 12, wherein the single packet switch
2 control is structured to utilize an algorithm that takes into account at least a current state
3 of each of the plurality of transcoders and a current load of all of the plurality of
4 transcoders.